

12 □□□□□□□□□□ □□□□□□□□

□□□□□□ □□□□□□ 1 □□□□□□□□/□□□□□□□□□□

20 mm 100 mm

[illegible]

A diagram of a 16-bit register. It consists of two groups of 8 bits each. The first group is labeled '0' and the second group is labeled '1'. The bits are represented by boxes, and the labels are placed below the corresponding groups of boxes.



11

問:	何年何月何日
問:	HL 160136
問:	PVC/何年何月何日
問:	何年何月何日何分何秒
問:	何分何秒

XXXXXXXX	1. XXXXXX: XX 2. XXXXXXXXXXXX
XX	XX + XXXXXXX + X
XX:	XXXXXXXXUPSXXDHLXXTNTXXARMAXXXEMS XXXXXXX
XXXXXX:	1. X/PP XX12 XXXX 62 * 31 * 50 cm
XXXX XXXX	XXXXXX 25-30 XX(XXXXXX)
XXXXXXXXXX:	5-7 XXX
XX	FOB XXXXXXX
OEM XXX ODM:	1). XXXXXXXXXXXX: XX; 2). XXXXXXXXXXXX: XX; 3). XXXXXXXXXXXXXXXX: XX
XXXXXX	1. XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 2. XXXXXXXXXXXX 3. XXXXXXXXXXXX 4. XXXXXXX 5. XXXXXXXXXXXX 6. XXXXXXXXXXXXXXXXXXXXXXXX

XXXXXXXXXXXXXXXXXXXX



XXXXXXXXXX

1. 20 XXXXXXXXXXXXXXX 10 XXXXXXXXXXXXXXX
2. XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
3. XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

4. 環境規格(CE・ROHS・EN71) 対応状況